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Notice of Allowability	Application No.	Applicant(s)		
	10/750,807	KUBOTA, SHINPE	KUBOTA, SHINPEI	
	Examiner	Art Unit		
	Leon Flores	2611		
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in th or other appropriate communic GHTS. This application is sub	is application. If not include cation will be mailed in due	led course. <b>THIS</b>	
1. This communication is responsive to 4/27/2007.				
2. The allowed claim(s) is/are <u>1-13</u> .				
<ol> <li>Acknowledgment is made of a claim for foreign priority una)</li></ol>	been received. been received in Application N	No	ation from the	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a ENT of this application.	reply complying with the re	equirements	
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give	itted. Note the attached EXAM es reason(s) why the oath or de	INER'S AMENDMENT or I eclaration is deficient.	NOTICE OF	
5. CORRECTED DRAWINGS ( as "replacement sheets") mus	t be submitted.			
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached				
1)  hereto or 2)  to Paper No./Mail Date				
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date				
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the he header according to 37 CFR	drawings in the front (not th 1.121(d).	e back) of	
<ol> <li>DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT</li> </ol>	sit of BIOLOGICAL MATER FOR THE DEPOSIT OF BIOLO	IAL must be submitted. DGICAL MATERIAL.	Note the	
Attachment(s)	E	mal Patant Application		
<ol> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftperson's Patent Drawing Review (PTO-948)</li> </ol>	6. ☐ Interview Sum	mal Patent Application		
	Paper No./Ma	ail Date		
<ol> <li>Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date</li> </ol>	7. 🔲 Examiner's Ar	nendment/Comment		
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's St 9. ☐ Other	atement of Reasons for All	owance	
DAVID C. PAYNE SUPERVISORY PATENT EXAMINER				

Application/Control Number: 10/750,807

Art Unit: 2611

## **DETAILED ACTION**

## Allowable Subject Matter

- 1. Claims 1-13 are allowed.
- The following is an examiner's statement of reasons for allowance. The art of record does not suggest the respective claim combinations together and nor would the respective claim combinations be obvious with:
- 3. Re claim 1, the further limitation of "a receiving apparatus comprising: at least one variable gain amplifier; at least one level detection circuit, which detects at least one level output from said at least one variable gain amplifier; at least one comparing circuit for comparing at least one reference level to said at least one output from said at least one level detection circuit; at least one demodulator; at least one binarizing circuit; at least one slice level holding circuit holding at least one substantially constant value at least one slice level employed by said at least one binarizing circuit; and at least one counter circuit; wherein at least one gain of said at least one variable gain amplifier is switched based on at least one result of comparison by said at least one comparing circuit; and wherein, when said at least one gain is switched, said at least one counter circuit and said at least one slice level holding circuit cause said at least one slice level to be held at said at least one substantially constant value for at least one prescribed time". Claims 2-7 depends on claim 1 above.
- 4. Re claim 8, the further limitation of "a receiving apparatus comprising: a variable gain amplifier; a level detection circuit for detecting an output of the variable gain amplifier; a comparing circuit for comparing a reference level to said output from said

Application/Control Number: 10/750,807

Art Unit: 2611

level detection circuit and outputting a gain setting signal comprising a positive gain setting signal or a negative gain setting signal; a demodulator; a binarizing circuit; a slice level holding circuit holding a slice level employed by said binarizing circuit at a substantially constant value; and a counter circuit receiving a first input when said comparing circuit outputs the positive gain setting signal and a second input when said comparing circuit outputs the negative gain setting signal, the counter circuit being operatively connected to said slice level holding circuit; said counter circuit causing said slice level holding circuit to hold the slice level at the substantially constant value for a given time when said first input changes to said second input or when said second input changes to said first input". Claims 9-10 depends on claim 8 above.

Re claim 11, the further limitation of "a receiving apparatus comprising: a variable gain amplifier; a level detection circuit for detecting an output of the variable gain amplifier; a comparing circuit for comparing a reference level to said output from said level detection circuit and outputting a gain setting signal comprising a positive gain setting signal or a negative gain setting signal; a demodulator; a binarizing circuit; means for holding a slice level employed by said binarizing circuit at a substantially constant value; and a counter circuit receiving a first input when said comparing circuit outputs the positive gain setting signal and a second input when said comparing circuit outputs the negative gain setting signal, the counter circuit being operatively connected to said means for holding a slice level employed by said binarizing circuit at a substantially constant value; said counter circuit causing said means for holding a slice level employed by said binarizing circuit at a

Application/Control Number: 10/750,807

Art Unit: 2611

slice level at the substantially constant value for a given time when said first input changes to said second input or when said second input changes to said first input".

Claims 12-13 depends on claim 11 above.

6. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Page 5

Application/Control Number: 10/750,807

Art Unit: 2611

## Contact

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Leon Flores whose telephone number is 571-270-1201. The examiner can normally be reached on Mon-Fri 7-5pm Alternate Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Payne can be reached on 571-272-3024. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

LF June 4, 2007

DAVID C. PAYNE
SUPERVISORY PATENT EXAMINER